Matter: Visio5-DRAFT04_41:0-4002;Utility;AMS for Acknowledgement independent Equalized AIEM Node Controller Crypto Device Conventional Computer Systemization 1102 1128 Cryptographic Processor Interface 00000000 Peripheral 0000000 1127 CPU Device(s) Clock Input Output 1103 1112 1130 Interface (I/O) 00000000 1108 User Input Device(s) 1111 Interface Bus System Bus 1107 1104 Crypto 1126 Network Interface ROM Communica 1110 RAM tions Network 1105 1106 00000000 Storage Interface 1113 1109 Storage Device 1114 FEC Cache Shared Data 1133b 1133a Acknowledgment Independent Equalized Data Packet Transfer Mechanism (AIEM) Module Mesh Table 1119 Cryptographic Server Module 1120 1118 Web Browser Module Information Server Module 1116 1117 User Interface Module 1115 Operating System (OS) Module

Figure 1

Memory

Centralized Controller

1101

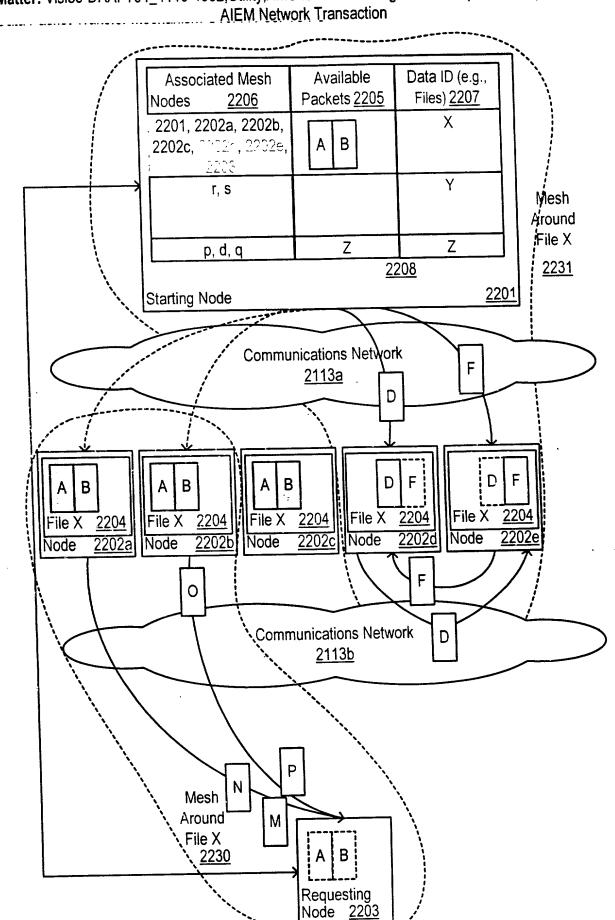
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Figure 2

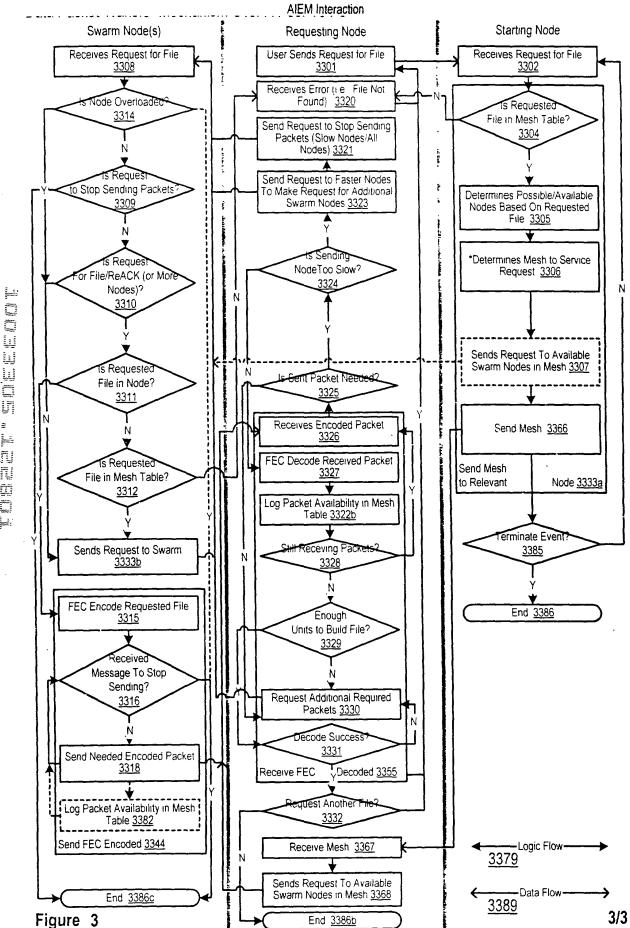
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